

AMENDMENTS TO THE ABSTRACT

Please amend the abstract as follows:

Abstract of the Disclosure

A method and apparatus for testing a processor is described. In one embodiment, a NOP ~~PSR~~ pseudo-random generator generates ~~error~~ error-checking NOP instructions during a pipeline stall. The insertion of these numerous NOP instructions ensures ~~the~~ the fact that the checking hardware checks more frequently and has more locations to check. Also, taking advantage of modern processor optimizations for handling NOPs minimizes any impact on processor performance.